

HD74LV1GT66A

Analog Switch

REJ03D0121-0800 Rev.8.00 Mar 21, 2008

Description

The HD74LV1GT66A has an analog switch in a 5 pin package. Switch section has its enable input control (C). High-level voltage applied to C turns on the switch section. Applications include signal gating, chopping, modulation, or demodulation (modem), and signal multiplexing for analog to digital and digital to analog conversion systems. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

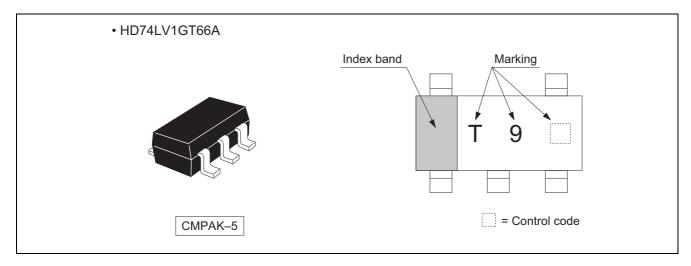
Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Control input is TTL compatible input level.
 - Supply voltage range: 3.0 to 5.5 V
 - Operating temperature range : -40 to +85°C
- Control inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V)
- Control inputs have hysteresis voltage for the slow transition.
- Ordering Information

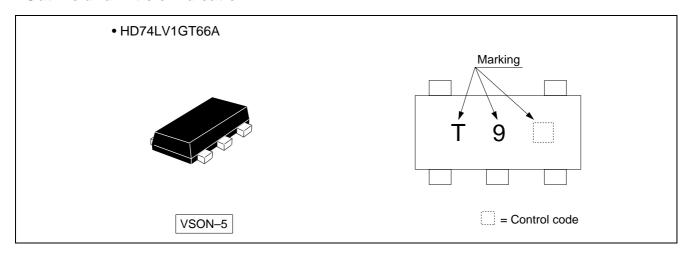
Part Name	Part Name Package Type		Package Abbreviation	Taping Abbreviation (Quantity)	
HD74LV1GT66ACME	CMPAK-5 pin	PTSP0005ZC-A (CMPAK-5V)	СМ	E (3000 pcs/reel)	
HD74LV1GT66AVSE	VSON-5 pin	PUSN0005KA-A (TNP-5DV)	VS	E (3000 pcs/reel)	

Note: Please consult the sales office for the above package availability.

Outline and Article Indication



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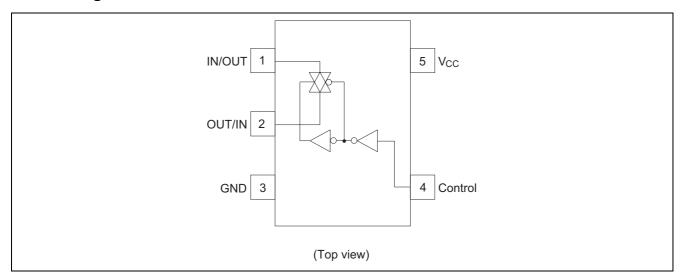


Function Table

Control	Switch
L	OFF
Н	ON

H : High level L : Low level

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{CC}	-0.5 to 7.0	V	
Input voltage range *1	Vı	-0.5 to 7.0	V	
Output voltage range *1, 2	Vo	-0.5 to V _{CC} + 0.5	V	Output : H or L
Input clamp current	I _{IK}	-20	mA	V ₁ < 0
Output clamp current	I _{OK}	±50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	I _O	±25	mA	$V_O = 0$ to V_{CC}
Continuous current through V _{CC} or GND	I _{CC} or I _{GND}	±50	mA	
Maximum power dissipation at Ta = 25°C (in still air) *3	P _T	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V_{CC}	3.0	5.5	V	
Input voltage range	VI	0	5.5	V	
Input / output voltage range	$V_{I/O}$	0	V _{CC}	V	
Input transition rise or fall rate	Δt / Δν	0	100	ns / V	$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
	Δt / Δν	0	20	115 / V	V _{CC} = 4.5 to 5.5 V
Operating free-air temperature	Ta	-40	85	°C	

Note: Unused or floating control inputs must be held high or low.

Electrical Characteristics

Item	Cumala al	V 00	T	a = 25°	С	T _a =	$T_a = -40 \text{ to } 85^{\circ}\text{C}$		Unit	Took Conditions	
ILCIII	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Тур	Max	Unit	Test Conditions	
	.,	3.0 to 3.6	_	_	_	1.5	_	_			
Innut voltage	V_{IH}	4.5 to 5.5	_	_	_	2.0	_	_	V	Control input only	
Input voltage	V _{IL}	3.0 to 3.6	_	_	_	_	_	0.6	V	Control input only	
	V IL	4.5 to 5.5	_	_	_	_	_	0.8			
Hysteresis	V	3.3	_	_	_	_	0.10	_	V	$V_T^+ - V_T^-$	
voltage	V_{H}	5.0	_	_	_	_	0.15	_	V	$V_T - V_T$	
On-state switch		3.0	_	50	150	_	_	190		$V_{IN} = V_{CC}$ or GND	
resistance	R _{ON}	4.5	_	40	75	_	_	100	Ω	$V_C = V_{IH}$ $I_T = 1 \text{ mA}$	
Dook on		3.0	_	100	180	_	_	225	Ω	$V_{IN} = V_{CC}$ to GND	
Peak on resistance	R _{ON (P)}	4.5	_	50	100	_	_	125		$V_C = V_{IH}$ $I_T = 1 \text{ mA}$	
Off-state switch leakage current	I _{s (OFF)}	5.5	_	_	±0.1	_	_	±1.0	μΑ	$V_{IN} = V_{CC}, V_{OUT} = GND$ or $V_{IN} = GND,$ $V_O = V_{CC}, V_C = V_{IL}$	
On-state switch leakage current	I _{s (ON)}	5.5	_	_	±0.1	_	_	±1.0	μΑ	$V_{IN} = V_{CC}$ or GND $V_C = V_{IH}$	
Input current	I _{IN}	0 to 5.5	_	_	±0.1	_	_	±1.0	μΑ	V _{IN} = 5.5 V or GND	
Quiescent	Icc	5.5	_	_	_	_	_	10	μΑ	$V_{IN} = V_{CC}$ or GND	
supply current	ΔI_{CC}	5.5	_	_	_	_	_	1.5	mA	V _{IN} = 3.4 V	
Control input capacitance	C _{IC}	_	_	3.5	_	_	_	_	pF		
Switch terminal capacitance	C _{IN / OUT}	_	_	4.0	_	_	_	_	pF		
Feed through capacitance	C _{IN-OUT}	_		0.5	_	_	_	_	pF		

Switching Characteristics

• $V_{CC} = 3.3 \pm 0.3 \text{ V}$

Item	Cumbal	Ta = 25°C			Ta = -40 to 85°C		Unit	Test	FROM	то
item	Symbol	Min	Тур	Max	Min	Max	Offic		(Input)	(Output)
Propagation	t _{PLH}	_	1.5	6.0	_	10.0	no	$C_L = 15 pF$	IN/OUT	OUT/IN
delay time	t _{PHL}	_	4.0	9.0	_	12.0	ns	$C_L = 50 pF$	or OUT/IN	or IN/OUT
Enable time	t _{ZH}	_	4.0	11.0	_	15.0	20	$C_L = 15 pF$	C	IN/OUT
Enable line	t_{ZL}	_	6.0	18.0 —	22.0	ns	$C_L = 50 pF$	C	or OUT/IN	
Disable time	t _{HZ}	_	5.0	11.0	_	15.0		$C_L = 15 pF$	0	IN/OUT
Disable time	t_{LZ}	_	8.0	18.0	_	22.0	ns	$C_L = 50 pF$	С	or OUT/IN

• $V_{CC} = 5.0 \pm 0.5 \text{ V}$

lt a see	Complete	Ta = 25°C			Ta = -40 to 85°C		l lmi4	Test	FROM	ТО
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	1.0	4.0	_	7.0	20	C _L = 15 pF	IN/OUT	OUT/IN
delay time	t _{PHL}	_	3.0	6.0	_	8.0	ns	C _L = 50 pF	or OUT/IN	or IN/OUT
E 11 ¢	t _{ZH}	_	3.0	7.0	_	10.0		C _L = 15 pF	C:	IN/OUT
Enable time	t _{ZL}	_	5.0	12.0	_	16.0	ns	C _L = 50 pF		or OUT/IN
Disable time	t _{HZ}	_	4.0	7.0	_	10.0	20	C _L = 15 pF	C	IN/OUT
	t_{LZ}	_	6.0	12.0	_	16.0	ns	C _L = 50 pF	С	or OUT/IN

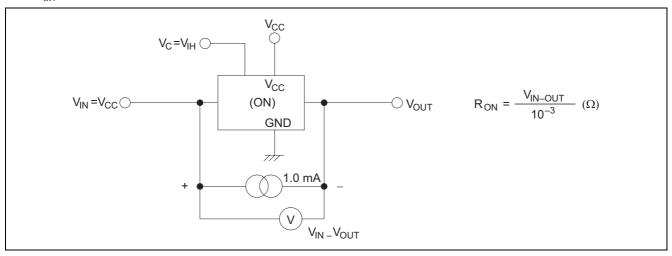
Operating Characteristics

• $C_L = 50 \text{ pF}$

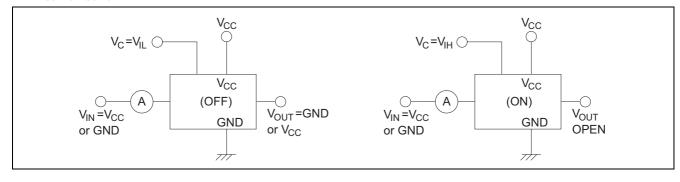
Item	Symbol	V _{CC} (V)	Ta = 25°C			Unit	Test Conditions
			Min	Тур	Max	Onit	rest Conditions
Power dissipation capacitance	C _{PD}	5.0		4.0		pF	f = 10 MHz

Test Circuit

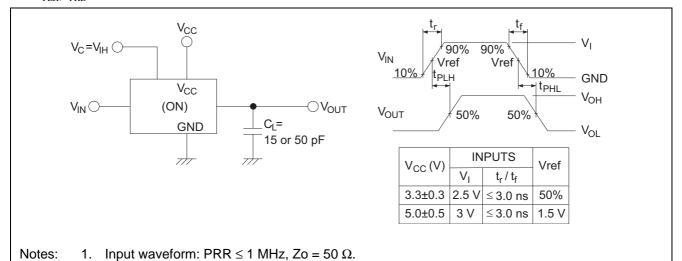
• R_{ON}



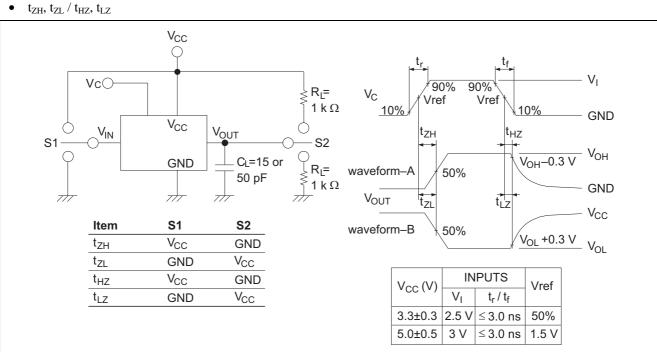
• $I_S(off)$, $I_S(on)$



• t_{PLH}, t_{PHL}

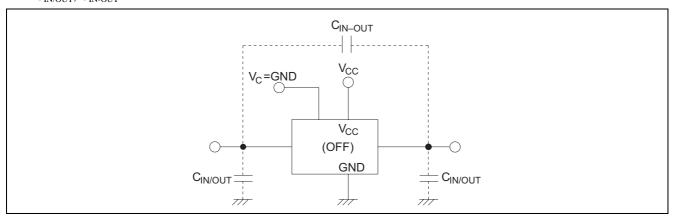


The output are measured one at a time with one transition per measurement.

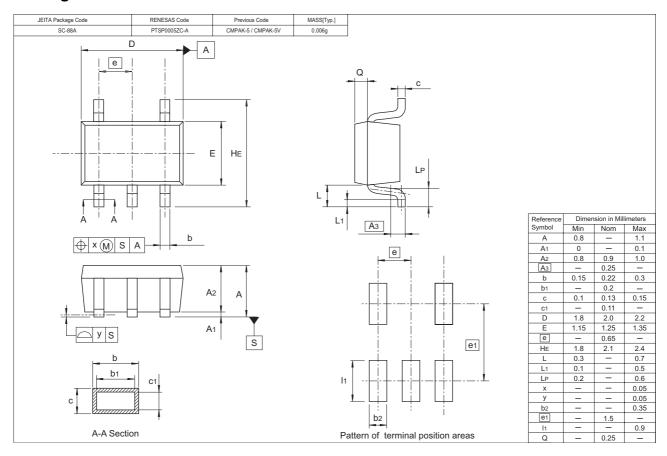


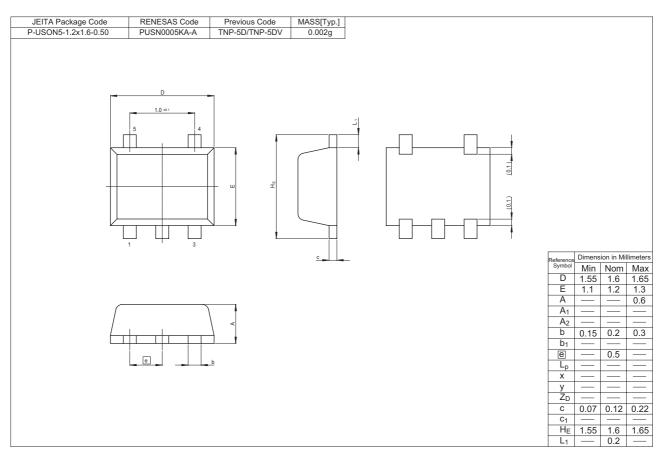
- Notes:
- 1. Input waveform: PRR \leq 1 MHz, Zo = 50 Ω .
- 2. Waveform—A is for an output with internal conditions such that the output is low except when disabled by the output control.
- 3. Waveform—B is for an output with internal conditions such that the output is high except when disabled by the output control.
- 4. The output are measured one at a time with one transition per measurement.

• C_{IN/OUT}, C_{IN-OUT}



Package Dimensions





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